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2814

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PTO/SB/21 (6-98)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/881,408
	Filing Date	June 13, 2001
	First Named Inventor	Kie Y. Ahn
	Group Art Unit	2814
	Examiner Name	T. Le
Total Number of Pages in This Submission		Attorney Docket Number MI22-1534

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	James E. Lake Reg. No. 44,854; Wells St. John P.S.
Signature	
Date	28 May 2002

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Appl. No. 09/881,408

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/881,408
Filing Date June 13, 2001
Inventor Kie Y. Ahn, et al
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner T. Le
Attorney's Docket No. MI22-1534
Title: A Dielectric Layer Forming Method and Devices Formed Therewith

RESPONSE TO FEBRUARY 26, 2002 OFFICE ACTION

To: Box NON FEE AMENDMENT
Assistant Commissioner for Patents
Washington, D.C. 20231

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)
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AMENDMENTS

In the Specification

Please replace paragraph 35 beginning at page 19 with the following clean replacement paragraph in accordance with 37 C.F.R. § 1.121(b)(1)(ii):

A'

--Referring to Fig. 3, an MOS transistor 14 formed in accordance with embodiments of the present invention is depicted. Gate dielectric 60 is disposed over a portion of